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PATENT APPLICATION

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of

Attn: OIPE

Ken YOSHIZAWA

Group Art Unit: 1722

Application No.: 10/565,108

Docket No.: 126623

Filed: January 19, 2006

For: A METHOD FOR PRODUCING A SILICON WAFER AND A SILICON WAFER

REQUEST FOR CORRECTION OF PALM RECORDS

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

Attached is a photocopy of the original filing receipt on which errors have been corrected in red. These errors are being brought to the attention of the Patent and Trademark Office so that it may correct its records.

Respectfully submitted,

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WPB:RBI/emt

Date: September 7, 2006

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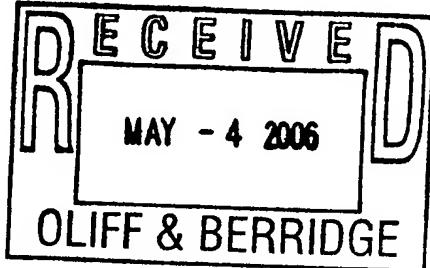
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APPL NO.	FILING OR 371 (c) DATE	ART UNIT	FIL FEE REC'D	ATTY.DOCKET NO	DRAWINGS	TOT CLMS	IND CLMS
10/565,108	01/19/2006	1722	900	126623	3	8	1

25944
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CONFIRMATION NO. 5486

FILING RECEIPT



OC000000018650539

Date Mailed: 05/01/2006

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Applicant(s)

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Assignment For Published Patent Application

SHIN-ETSU HANDOTAI CO., LTD, Tokyo, JAPAN

Power of Attorney: The patent practitioners associated with Customer Number 25944.

Domestic Priority data as claimed by applicant

This application is a 371 of PCT/JP04/09921 07/12/2004

Foreign Applications

JAPAN 2003-278483 07/23/2003

If Required, Foreign Filing License Granted: 04/28/2006

The country code and number of your priority application, to be used for filing abroad under the Paris Convention, is **US10/565,108**

Projected Publication Date: 08/10/2006

Non-Publication Request: No

Early Publication Request: No

Title

producing
Method for producing silicon wafer and silicon wafer

Preliminary Class

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